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The Japanese National Research Grid Initiative (NAREGI) is a 5-year project covering the period 2003 to 2007, funded by the Ministry of Education, Sports, Culture, Science and Technology. In March 2004, a 10-TFLOPS-class computer facility was installed at the Computational Nano-science Center of the Institute for Molecular Science and a 5-TFLOPS-class computer facility was installed at the Center for Grid Research and Development of the National Institute of Informatics. These computer facilities, together with the computer resources at the collaborating research organizations of NAREGI project, will be connected to SuperSINET to constitute the NAREGI Testbed.

(P.732 "Recent Trends in Grid Computing" K. Miura)